

Docket No.: GR 99 P 2378

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on the date indicated below.

By: m p m

Date: 3/6/01



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Xiaopin Zhang et al.
Applic. No. : 09/624,438
Filed : July 24, 2000
Title : Controlable Current Source Circuit and Phase Locked Loop
Equipped Therewith
Art Unit : 2787

RECEIVED
MAR 12 2001
Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

German Published, Non-Prosecuted Patent Application DE 199 52 867 A1 (Hietala et al.), dated May 18, 2000, phase detector with frequency control;

Japanese Patent Application and Abstract JP 1-128621 (Eiji), dated May 22, 1989.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

m p m
For Applicants

Date: 3/6/01

Lerner And Greenberg, P.A.

Post Office Box 2480

Hollywood, FL 33022-2480

Tel: (954) 925-1100

Fax: (954) 925-1101

bmb